MEIG
股票代码:002881

全球领先的物联网终端及无线数据方案提供商

SLM756 [illegible]

[illegible] E 2020 6

[illegible] ł o E SLM756 [illegible]

[illegible] EV1.02

[illegible] E [illegible]

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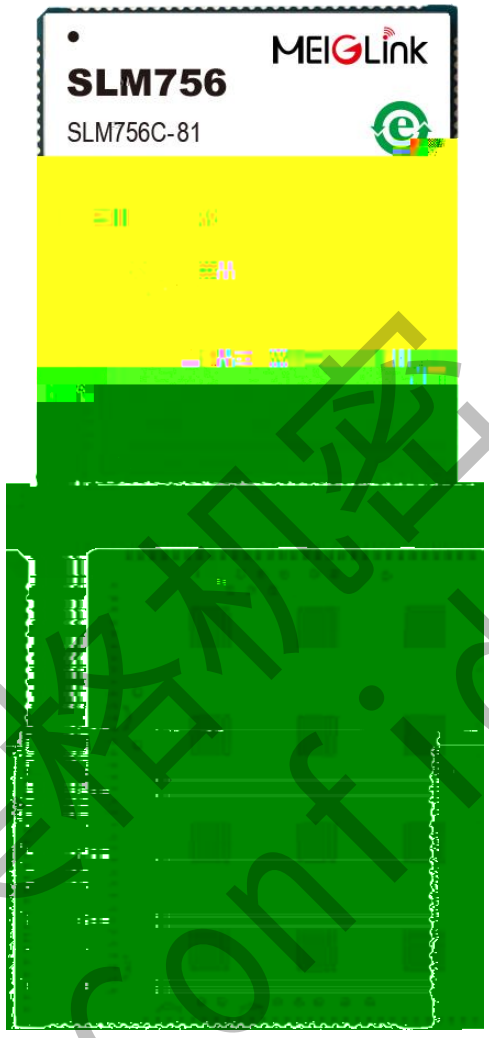
E

D VM



	D
	M

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D

D ME

pin#	pin name	Pad characteristics	Functional description
1	MIPI_DSI0_LANE3_N	AI, AO	

16	MIPI_CSI0_LANE0_N	

51	VBUS	PI	USB VBUS Voltage
52	KYPD_PWR_N	DI	Power on key
53	GND	GND	GND
54	RF_WIFI/BT	AI,AO	RF signal
55	GND	GND	GND
56	GND	GND	GND
57	PM_RESIN_N	DI	PMIC reset
58	GPIO7_I2C_SCL	DO;B-PD:nppukp	Configurable I/O I2C or GPIO
59	GPIO6_I2C_SDA	DO;B-PD:nppukp	Configurable I/O I2C or GPIO
60	GND	GND	GND
61	RF_GPS	AI	RF signal-GPS ANT
62	GND	GND	GND

63

87	GND	GND	GND
88	UIM1_DET_N	DI,B-PD:nppukp	Configurable I/O UIM1 removal detection
89	GPIO3	B-PD:nppukp	Configurable I/O
90	GPIO2	B-PD:nppukp	Configurable I/O
91	GPIO1	B-PD:nppukp	Configurable I/O
92	GPIO0	B-PD:nppukp	Configurable I/O
93	GND	GND	GND
94	[redacted]	PO	PMIC output 2.95V
95	SDC2_SDCARD_DET		

122	MIPI_DSI0_LANE0_P	AI, AO	MIPI display serial interface 0 lane 0 –positive
123	MIPI_DSI0_LANE1_N	AI, AO	MIPI display serial interface 0 lane 1 – negative
124	MIPI_DSI0_LANE1_P	AI, AO	MIPI display serial interface 0 lane 1 –positive
125	MIPI_DSI0_LANE2_N	AI, AO	MIPI display serial interface 0 lane 2 –negative
126	MIPI_DSI0_LANE2_P	AI, AO	MIPI display serial interface 0 lane 2 –positive
127	GPIO36	B-PD:nppukp	Configurable I/O

128

	-25	-	75	
	-40	-	90	

4.3

4.3

VBAT	3.4		4.2	V
VBUS	4	5	6	V
	2.5	2.8	-	V

4.4

4.4 1.8V

V_{IH}		1.17	-	-	V
V_{IL}		-	-	0.63	V
V_{OH}		1.35	-	-	V
V_{OL}		-	-	0.45	V

4.5SIM_VDD

4.5 SIM_VDD

V_O		-	3	-	V
		-	1.8	-	
I_O		-	-	55	mA

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4.6PWRKEY

4.6 PWRKEY

PWRKEY		1.4	-	-	V
		-	-	0.6	V
		2000			ms

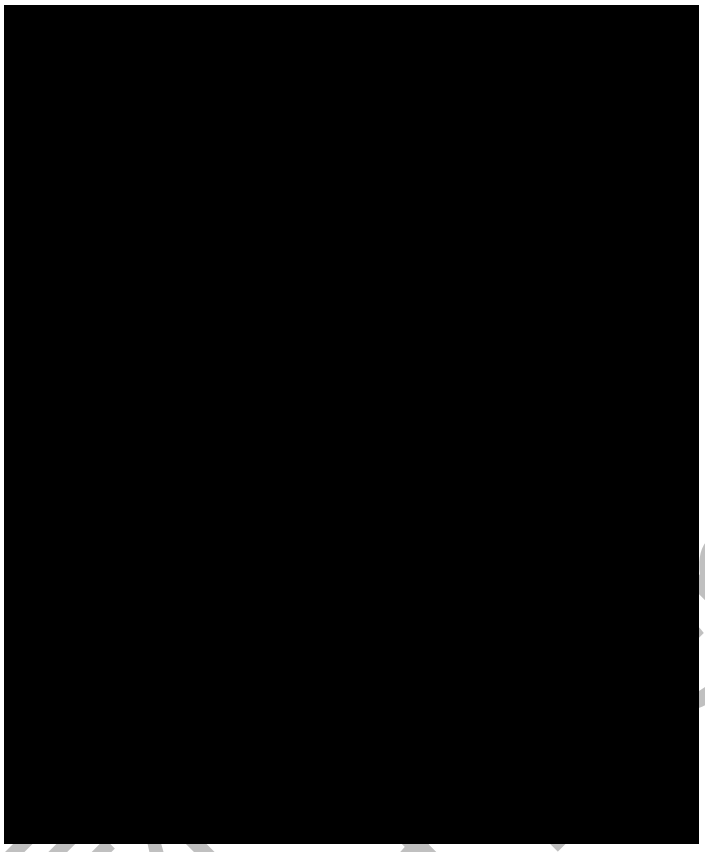
4.7VCOIN

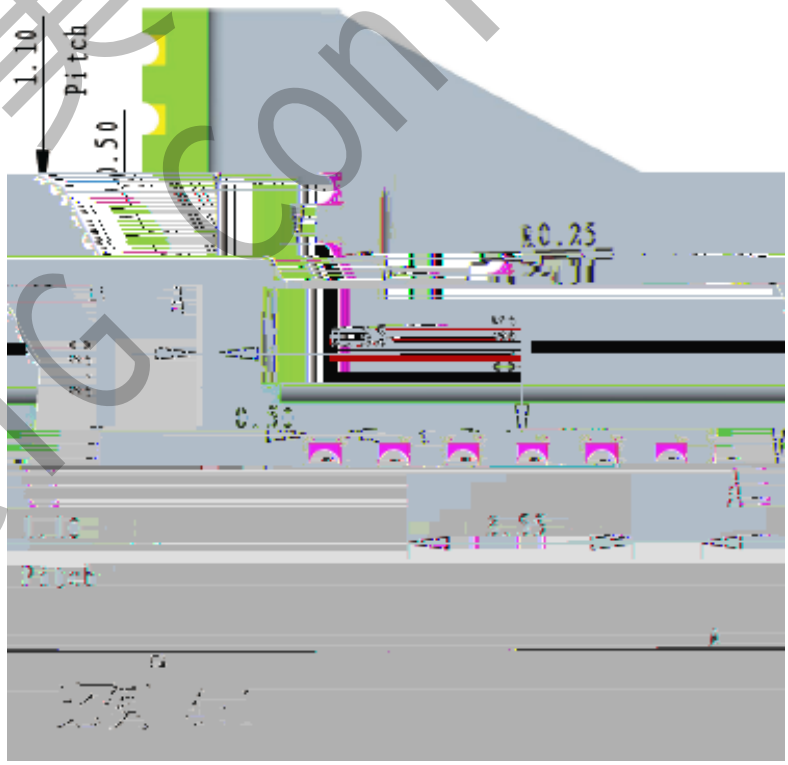
4.7 VCOIN

VCOIN-IN	VCOIN	2	3	3.25	V
IRTC-IN	VCOIN	-		8	uA
VCOIN-OUT	VCOIN	-	3	-	

		TD-S	-	-	3.0	mA
		CDMA	-	-	3.0	mA
		FDD			2.0	mA
		TDD			2.1	mA

TOP





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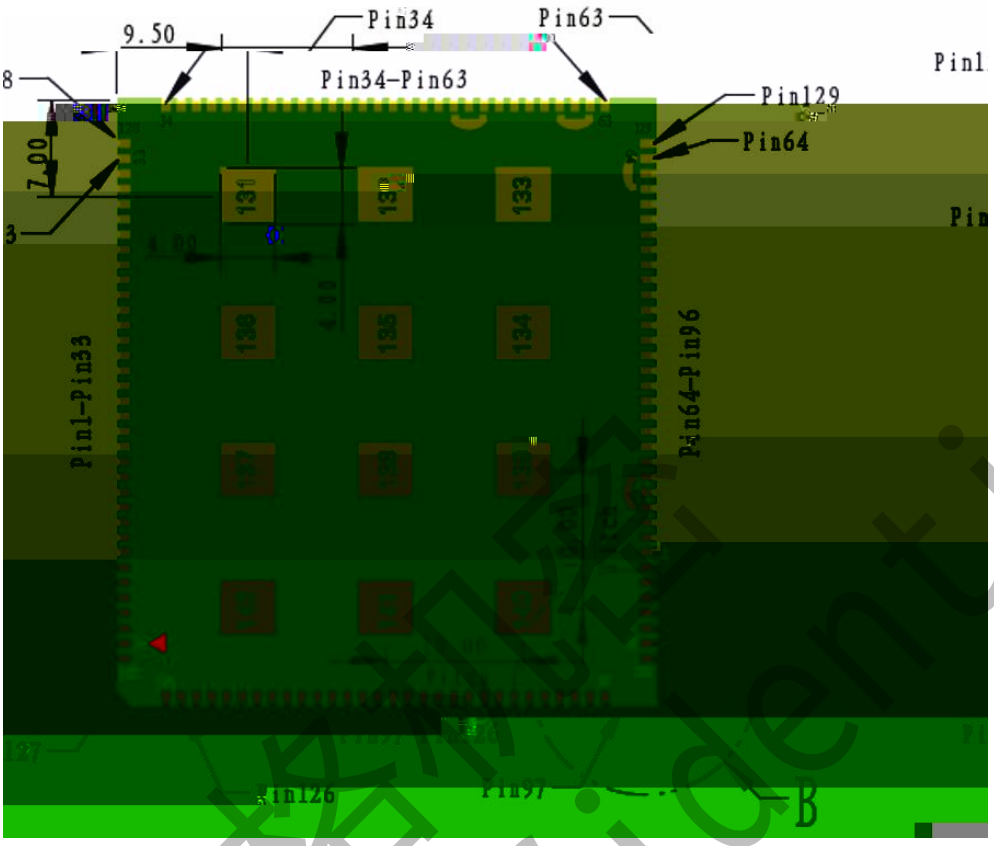

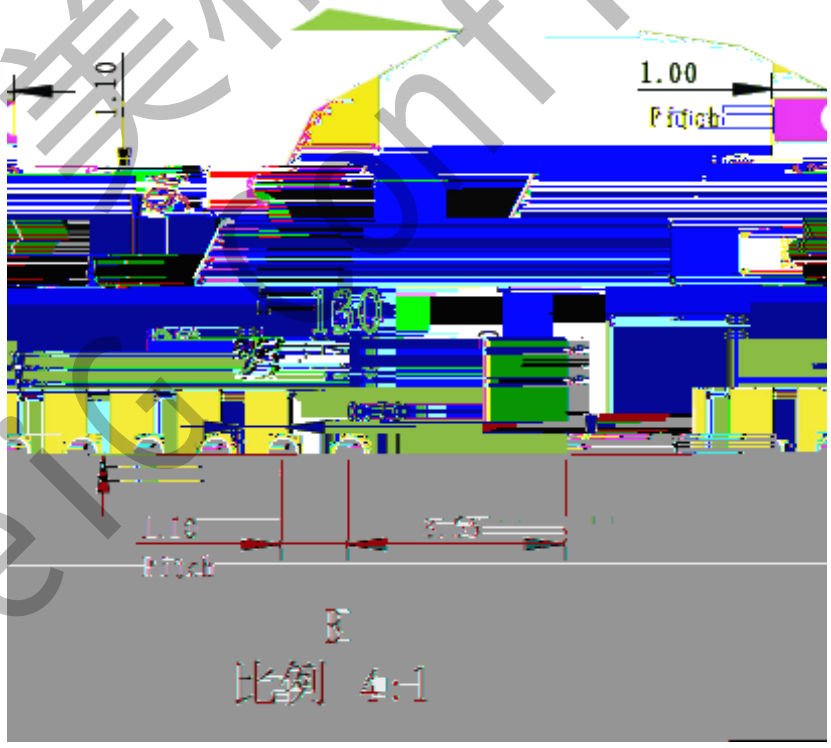

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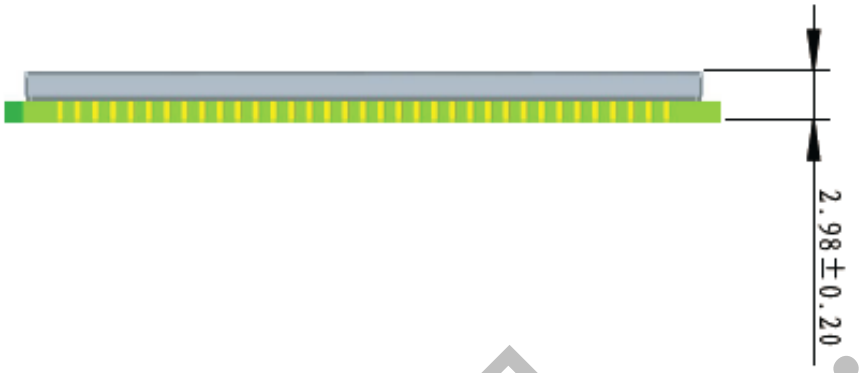

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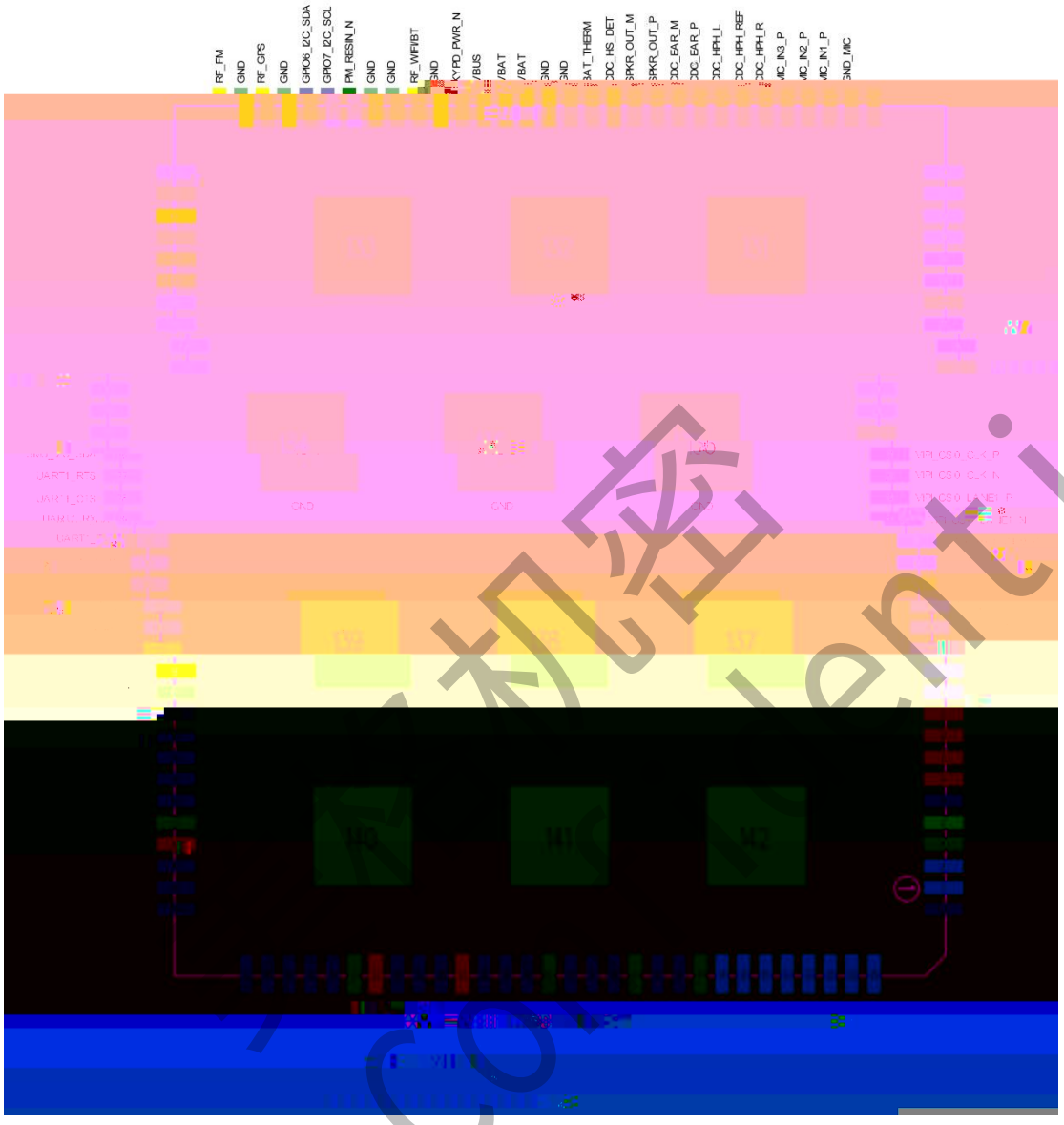

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